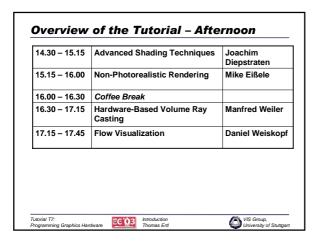
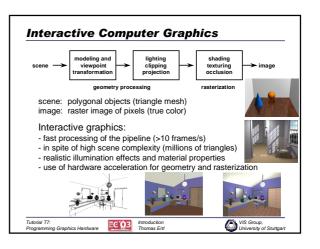
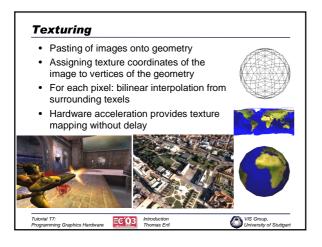
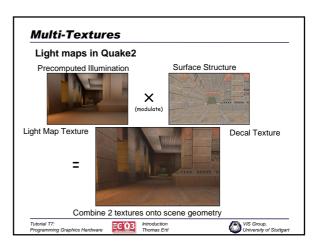


09.30 – 10.30	Introduction to the Tutorial	Thomas Ertl
10.30 – 11.00	Low-Level Vertex Shader Programming	Martin Kraus
11.00 - 11.30	Coffee Break	
11.30 – 12.00	Low-Level Pixel Shader Programming	Martin Kraus
12.00 – 12.45	High-Level Shading Languages	Daniel Weiskop
12.45 – 14.30	Lunch Break	

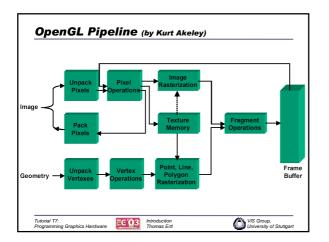


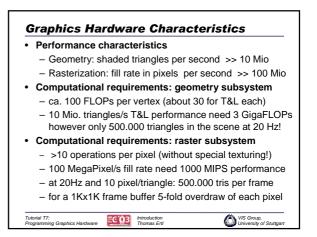


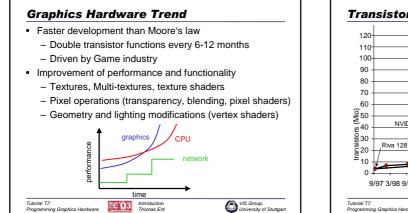


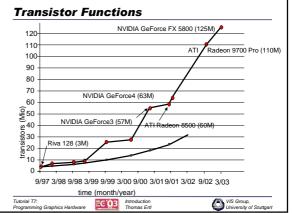












Brand:	ATI Radeon 9800 P	Nvidia GeforceFX 5900 U
 Transistors 	107 Mio	130 Mio
 Technology 	0.15 micron	0.13 micron
 Clock rate 	380 MHz	450 MHz
 Mem bandwidth 	22 GB/s	27 GB/s
 Fill rate (peak) 	3 GigaPixel/s	1.8/3.6 GigaPixel/s
 Pixel Pipelines 	8	4/8
 Textures per Unit 	8	16
• FSAA	6x 18 Gsample/s	4x 27 Gsample/s
 Bits per channel 	10	10
Tri transform (peak)	380 Mio	315 Mio
 Tris (3Dmark) 	19 Mio	28 Mio
Vertex shaders	4	4+
		www.tomshardware.de
Tutorial T7: Programming Graphics Hardware	CO3 Introduction Thomas Ertl	VIS Group, University of Stuttaer

•	1980s : Simple rasterization
	(bitBLT, windows, lines, polygons, text fonts)
•	1990-95: Geometry engines only for high-end workstations (e.g. SGI O2 vs. Indigo2)
•	1995: New rasterization functionality
	(realism with textures) z.B: SGI Infinite Reality
•	1998: Geometry processing (T&L) for PC graphics cards
•	2000: PC graphics reaches high-end performance numbers, 3D becomes PC standard
•	2001: PC graphics offers additional functionality (multi-texturing, vertex and pixel shaders)
•	2003: Shading Languages: NVIDIA Cg, OpenGI 2.0, DX9 GPUs > 100 Mio. transistors, 8 Pipes and 16 texture units

